

LSST End-to-End Verification Environment

MagicDraw, Syndeia, and JIRA with Kanoah Tester

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MBSE Telescope Workshop II April 6, 2017

MBSE Telescope Workshop II • April 2017

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Agenda



Review of LSST's Verification Process
Overview of implementation tools

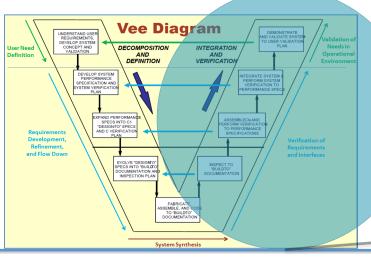
MagicDraw
Syndeia
JIRA
Kanoah Tester add-in

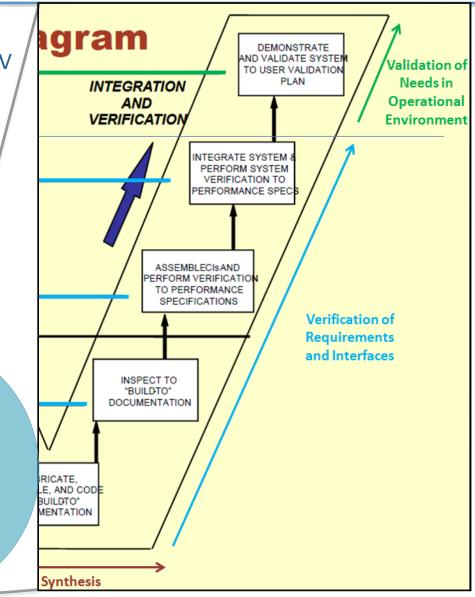
Demo Video
Future Work

Verification & Validation on LSST



- LSE-160 Verification and Validation Process is the governing document for V&V on LSST
 - Establishes a consistent, project-wide process for the development of V&V plans, compliance assessments, V&V reporting, and deliverables
- Defines steps in the verification process
- Defines requirements for developing verification plans for each projectcontrolled requirement

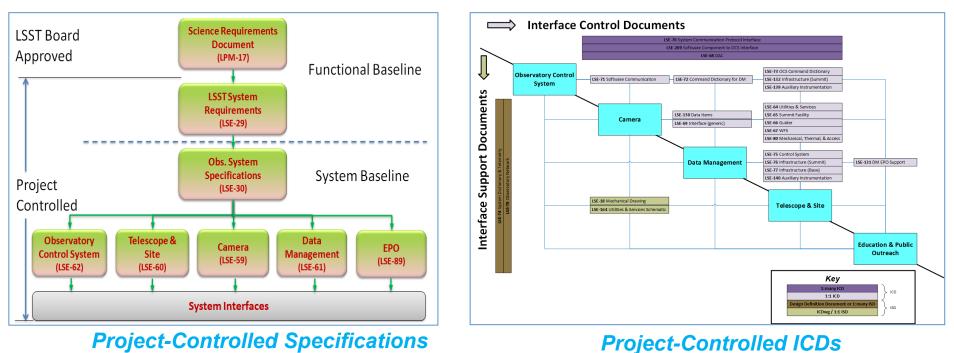




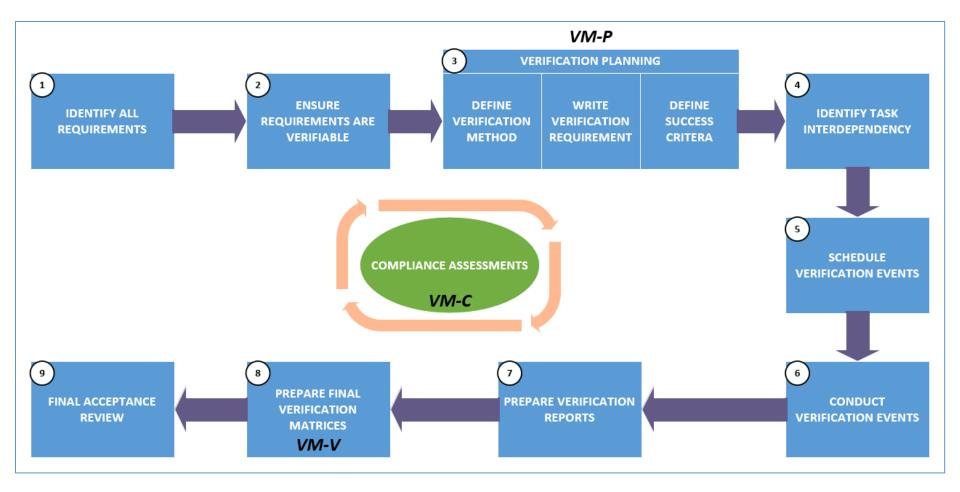
LSE-160 Applicability



- Applies to all Project-Controlled requirements:
 - Specifications
 - **ORequirements Documents**
 - **OInterface Control Documents (ICDs)**
- Each "shall" statement in each of these documents must be formally verified







See previous verification talk from November 2, 2016 MBSE Workshop for additional information

Process Overview



Requirements,

• Several tools are utilized to capture this information

• MagicDraw (Model Based Systems Engineering Tool) – Verification Plans

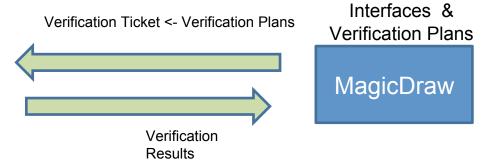
 JIRA (Issue Tracking & Agile Work Planning Tool) – Broader user base, ticketing capabilities, agile planning

Kanoah Tester (JIRA Testing Add-In) – Test Planning and Execution

Syndeia – MagicDraw/JIRA synchronization for Verification Plans

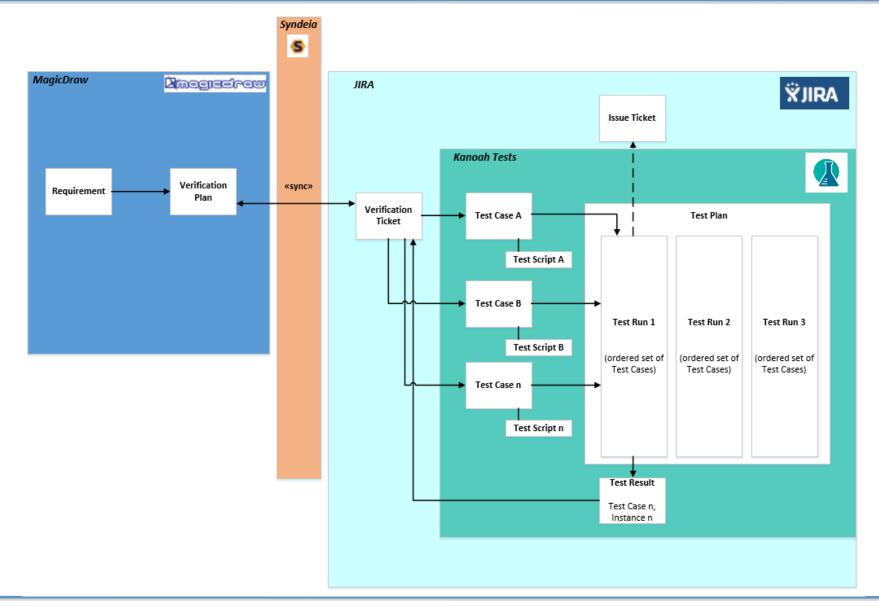
Test Case, Test Plan, and Test Run Development + Execution





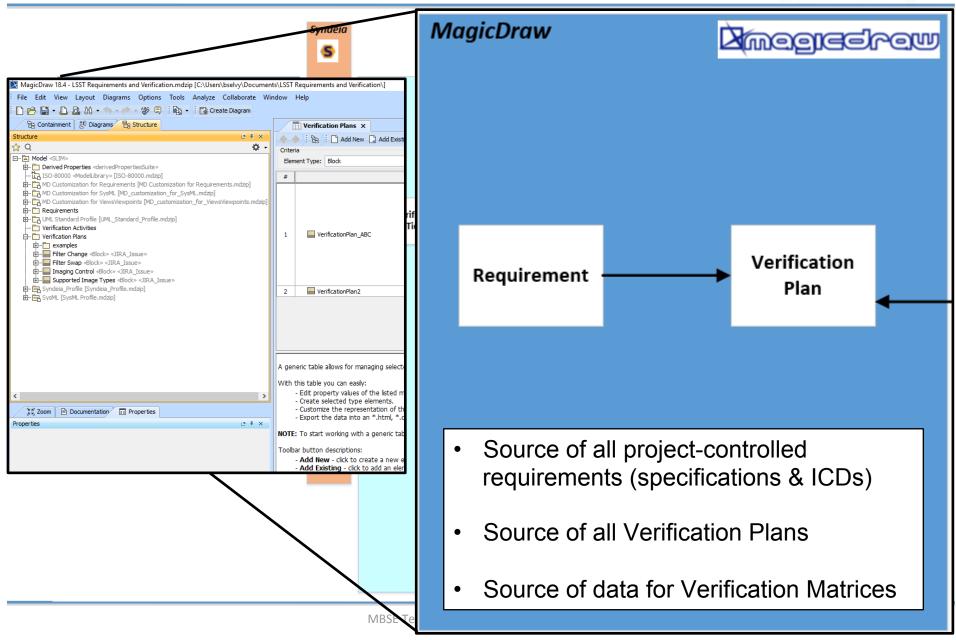
Verification Tools





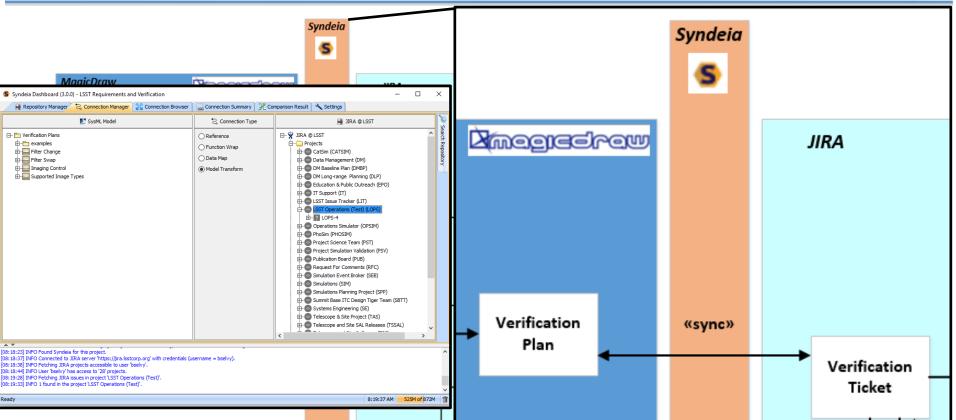
Verification Tools – MagicDraw MBSE Tool





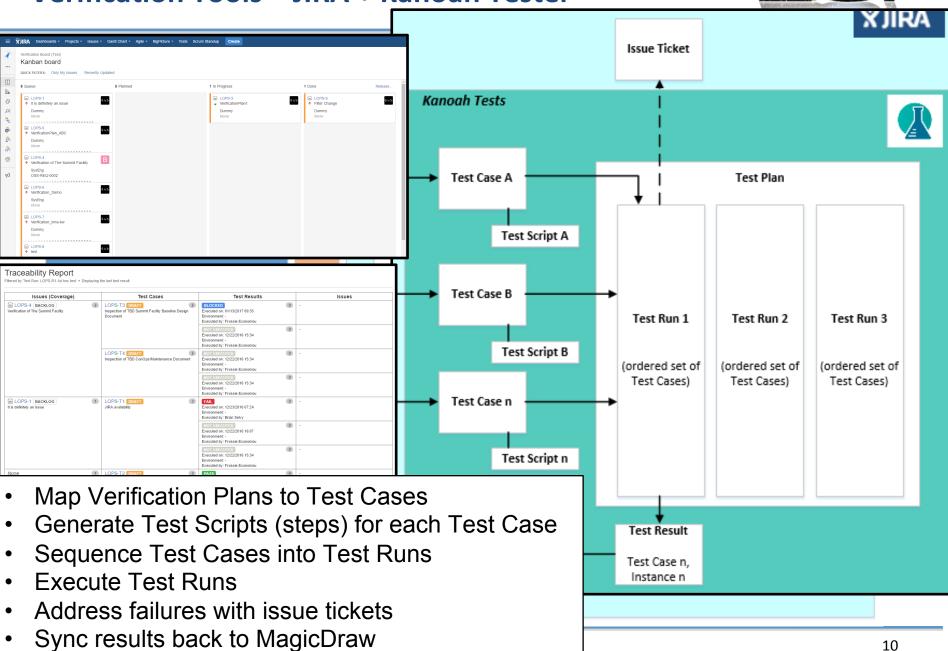
Verification Tools – Syndeia Inter-Tool Element Transformations



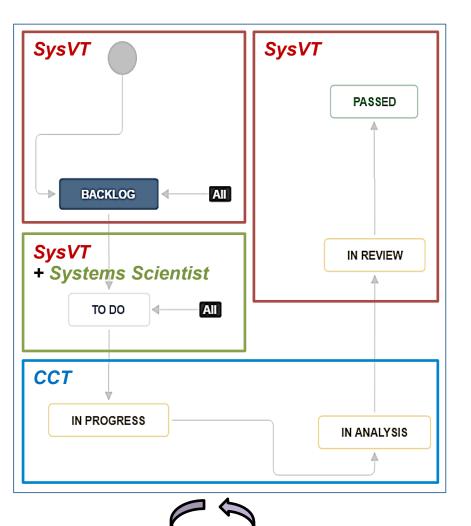


- Auto-generates Verification Tickets in JIRA from MagicDraw VerificationPlan elements and attributes
- Two-way synchronization
- Feeds verification results back to MagicDraw to generate final verification matrices (VM-F)

Verification Tools – JIRA + Kanoah Tester



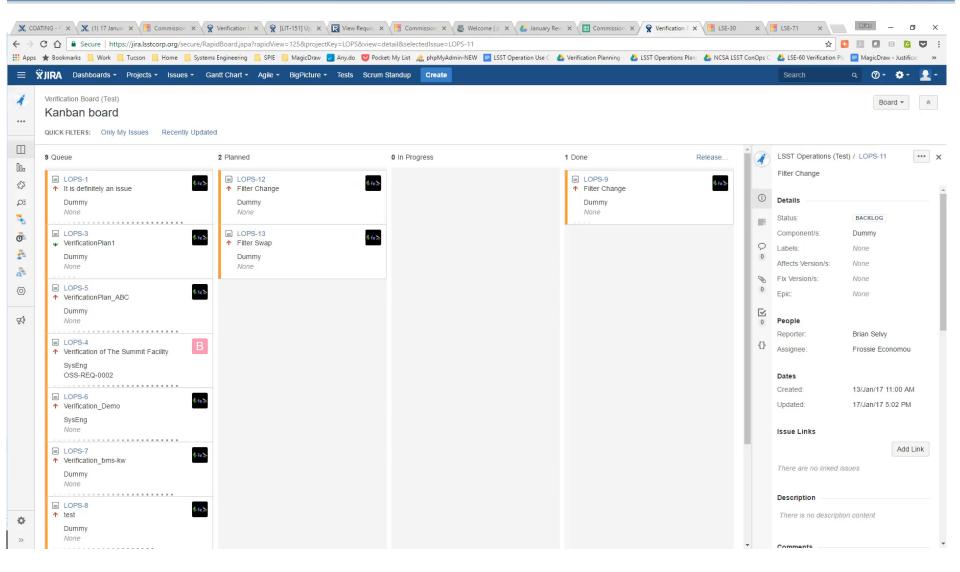




- System Verification Team (SysVT) responsible for upfront planning
 - •Generation of Verification Plans
 - Mapping to Verification Events (Test Cases)
 - **Operation of Verification Events**
- SysVT works with the Systems Scientist to Schedule activities
- Tickets get assigned to individuals responsible for the work. Core Commissioning Team (CCT) executes the Verification Events (Test Cases) and Analyzes Results
- SysVT Reviews the results and makes the determination on the sufficiency of the results to close out the Verification Event
- SysVT tracks verification progress

Prototype Tool





Future Work



- Develop requirements for test planning and execution tool to ensure Kanoah Tester meets all requirements (in work)
- Work with test execution tool vendor to modify tool to LSST's needs
- Begin using on subsystem AIV