Reduced Instruction Set Computer (RISC)

Return to Glossary

Reduced Instruction Set Computer (RISC) is a computer that uses a cpu that implements the processor design principle of simplified instructions. To date, RISC is the most efficient CPU architecture technology.

This architecture is an evolution and alternative to cisc. With RISC, the basic concept is to have simple instructions that do less but execute very quickly to provide better performance.

Source: Reduced Instruction Set Computer (RISC)